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CONFIRMATION NO. 7934

Bib Data Sheet

SERIAL NUMBER 09/904,663	FILING DATE 07/16/2001 RULE	CLASS 374	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. KAN 120D1
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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/434,490 11/05/1999 PAT 6,407,563

Verified for H

** FOREIGN APPLICATIONS *****

JAPAN JP11-66621 03/12/1999

verified for H

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 07/31/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 33	TOTAL CLAIMS 24 14	INDEPENDENT CLAIMS 22
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>John A. Hoyer</i> Examiner's Signature Initials				

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TITLE

Semiconductor device test method

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